

AMENDMENT TO THE CLAIMS

1. (currently amended) A method comprising a step (a) of determining a location within a cycle by reading a portion of a cyclic bit sequence, the bit sequence containing several interspersed bit-group sets that each contain a plurality of series that have servo sectors consecutively-placed between data sectors, wherein all of the servo sectors in each bit-group set includes~~that each consist of several consecutively placed identical~~ bit-groups having identical bits.

2. (original) The method of claim 1 in which the determining step (a) includes steps of:

(a1) reading several servo fields from a data surface, each of the servo fields consisting of a respective one of the bit-groups, an analog portion and a digital remainder portion; and

(a2) deriving a sector number from the bit-groups and not from the digital remainder portions, the sector number being the determined location within the cycle.

3. (original) The method of claim 1 further comprising a step (b) of accessing a sector having a sector number, the sector number being the determined location within the cycle.

4. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of writing each of the bit-groups as a mutually adjacent plurality of bits within a respective servo field.

5. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of selecting each of the sets so that each of the bit-groups in the set uniquely identifies the set.

6. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of writing several of the series consecutively and so that the series each consist of exactly S consecutively-placed bit-groups, where  $S > 3$ .

7. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of assembling a several data surfaces into a co-rotating assembly so that a pair of the data surfaces have a significant angular misalignment smaller than a predetermined threshold, at least one of the pair containing the cyclic bit sequence.

8. (original) The method of claim 1 in which the determining step (a) includes steps of:

- (a1) assembling first and second disc surfaces into a co-rotating assembly so that the surfaces have a significant angular misalignment, the second disc containing the cyclic bit sequence;
- (a2) measuring a first position on the first disc or surface;
- (a3) reading the cyclic bit sequence portion from the second disc or surface; and
- (a4) determining the location based on a combination of the first position from the measuring step (a2) and the sequence portion from the reading step (a3).

9. (original) The method of claim 1 in which the determining step (a) is completed within one disc revolution of activating a transducer that reads from the cyclic bit sequence.

10. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of writing each of the bit-groups within a few nominal bit-lengths of a respective track identifier.

11. (original) The method of claim 1 in which the determining step (a) includes steps of:

- (a1) reading a bit pattern from a data surface containing the cyclic bit sequence;
- (a2) verifying that the bit pattern from the reading step (a1) is consistent with the bit sequence; and
- (a3) accepting the bit pattern as the read portion based on the verification step (a2).

12. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of mounting two data surfaces so as to generate a significant angular misalignment therebetween, each of the data surfaces containing the cyclic bit sequence.

13. (original) The method of claim 1 in which the determining step (a) includes a step (a1) of detecting a plurality of inter-set transitions in the read portion of the bit sequence.

14. (previously withdrawn) A method comprising steps of:

- (a) writing many M-bit set labels each into a respective servo sector so as to define a cyclic bit sequence, where  $M > 1$ , the bit sequence containing several interspersed bit-group sets that each contain a plurality of series that each consist of 2 (circumflex over ()) $N(=S)$  of the set labels, where  $N > 1$ , each of the series consisting of the set labels of each of several circumferentially consecutive ones of the servo sectors, all of the set labels within each of the sets being identical;
- (b) assembling at least first and second discs onto a disc stack so that the discs are angularly aligned within  $\pm (2 \cdot \sup{M+N-1}) \cdot 360 / (W \cdot 2)$  degrees, the first disc having a data surface containing the cyclic bit sequence defined in the writing step (a);
- (c) reading several servo fields from the data surface of the assembling step (b), each of the servo fields consisting of a digital portion and an analog portion, each of the digital portions consisting of a set label and a remainder portion;
- (d) activating a transducer that reads at least some of the bit-groups and detects an inter-set transition; and
- (e) within one disc revolution of beginning the activating step (d), determining a current sector number on the data surface based on a combination of the inter-set transition and on a pre-switch position detected on the second disc, making this determination not based on the digital remainder portions.

15. (currently amended) An apparatus comprising at least one rotatable element containing a cyclic bit sequence, the bit sequence containing several interspersed bit-group sets that each contain a plurality of series that each consist of several circumferentially consecutive identical bit-groups, wherein each of the bit-groups resides in a respective nominally-contiguous digital portion of a respective servo sector, each digital portion is bound by two respective non-digital portions of the respective servo sector occupying a total area A, each of the bit groups occupying a smaller area B.

16. (original) The apparatus of claim 15, ~~in which each of the bit groups resides in a respective nominally contiguous digital portion of a respective servo sector, each of the digital portions being bounded by two respective non digital portions of the respective servo sector, each of the digital portions occupying a total area A, each of the bit groups occupying a~~ wherein smaller area B ~~that~~ is almost about 15% of A.

17. (original) The apparatus of claim 15 in which the cyclic bit sequence resides on one annular data surface of the rotatable element, in which the data surface includes a multitude of servo sectors each containing one of the bit-groups, the servo sectors occupying a total area A, the bit-groups occupying a smaller area  $B < 2\%$  of A.

18. (original) The apparatus of claim 15 in which each of the bit-groups is a set-identifying label that identifies a respective one of the several sets.

19. (original) The apparatus of claim 18 in which each of the bit-groups resides in a respective nominally-contiguous digital portion of a respective servo sector, each of the digital portions being bounded by two respective non-digital portions of the respective servo sector, each of the digital portions occupying a total area A, each of the bit-groups occupying a smaller area B that is at most about 15% of A.

20. (original) The apparatus of claim 18 in which the cyclic bit sequence resides on one annular data surface of the rotatable element, in which the data surface includes a multitude of servo sectors each containing one of the bit-groups, the servo sectors occupying a total area A, the bit-groups occupying a smaller area  $B < 2\%$  of A.

21. (original) The apparatus of claim 18, further comprising a controller configured to determine an angular location on the element by reading a plurality of the bit-groups.

22. (original) The apparatus of claim 15, further comprising a controller configured to determine an angular location on the element by reading a plurality of the bit-groups.

23. (original) The apparatus of claim 22 in which each of the bit-groups resides in a respective nominally-contiguous digital portion of a respective servo sector, each of the digital portions being bounded by two respective non-digital portions of the respective servo sector, each of the digital portions occupying a total area A, each of the bit-groups occupying a smaller area B that is at most about 15% of A.

24. (original) The apparatus of claim 22 in which the cyclic bit sequence resides on one annular data surface of the rotatable element, in which the data surface includes a multitude of servo sectors each containing one of the bit-groups, the servo sectors occupying a total area A, the bit-groups occupying a smaller area  $B < 2\%$  of A.

25. (previously presented) An apparatus comprising at least one rotatable element having at least one track and at least one cyclic bit sequence, the bit sequence containing ~~with servo sectors and~~ multiple interspersed bit-group sets that each contain a plurality of series that have servo sectors ~~consecutively-placed between data sectors, wherein all of the servo sectors in each bit-group set~~ includes of identical-bit-groups having identical bits distributed along the track, in which the identical bit-groups reside in selected consecutive ones of the servo sectors.